

REMARKSStatus of Claims

Applicants have canceled claims 1-52 and added claims 53-74. Claims 53-74 are the same as claims 72-93 of the parent reissue application, which were canceled without prejudice or disclaimer from the parent application under 37 C.F.R.

1.312(a).

Support for Claims Added by Amendment

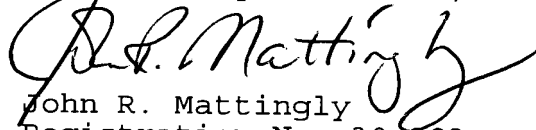
Claims 53-74 are supported by Figs. 12A-15C. Fig. 15A shows complementary MISFETs including P-channel and N-channel MISFETs T_{33} , T_{32} and a circuit block 61 that also includes an operating voltage V_{CL2} . In a first operation mode, V_{P1} is at 0 volts. In the second operation mode, V_{BP1} is at -1 volts, as shown in Fig. 15C.

Claim 66 is supported by Fig. 15A, which shows a first circuit block 60 including P-channel and N-channel MISFETs T_{31} and T_{30} , respectively. In a first operation mode, shown in Fig. 15C, V_{BP1} equals 0 volts.

Claim 70 is also supported by Fig. 15A and includes first and second circuit blocks 60, 61. Support for the remainder of the claim is as discussed above with respect to claims 53 and 66.

Applicants request examination of the newly added claims in view of the foregoing remarks. Reconsideration and reexamination are respectfully requested.

Respectfully submitted,



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